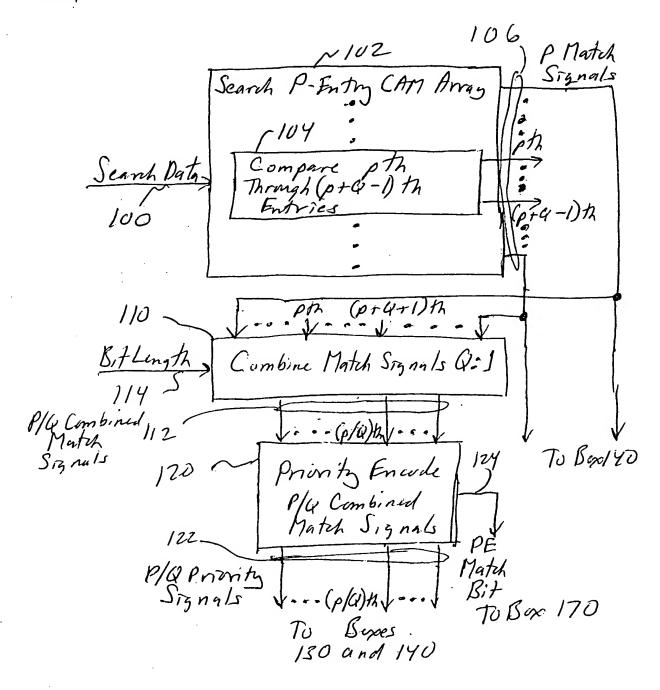
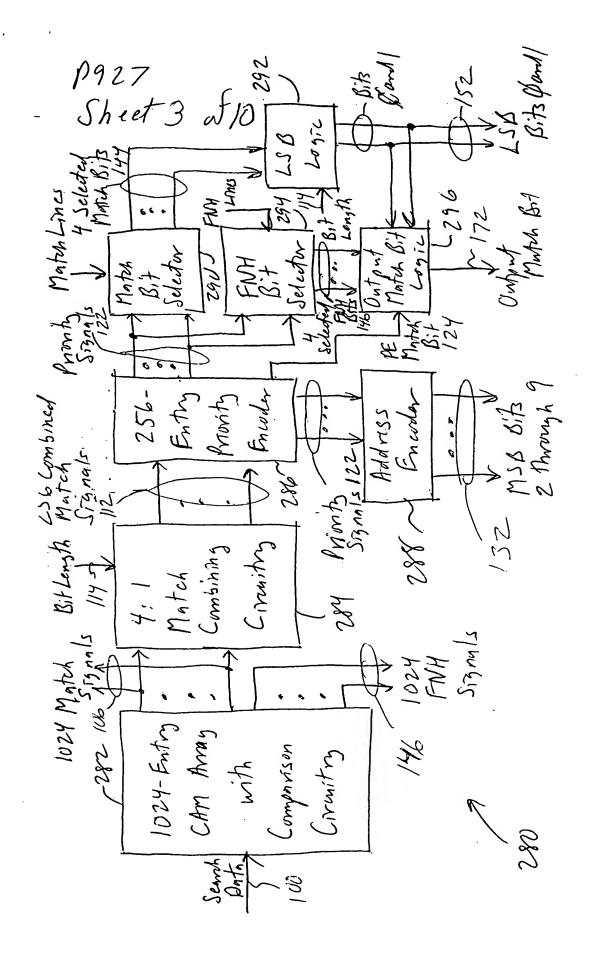
P927 Sheet 1 of 10



F19, 1

P927 Sheet 2 of 10 PMatch From Box 120 P/Q Privity From Box120 Signals 122 Frem Box Select Match and 140 Encode ·102 Suppress Signals for Pravita Printy Result Signals pth 142 (p+4-01) Q Selected P Suppress Match Signals Log2(1/4) MSBs 132 Signals eg. FM Q Selected Bits Suppress P Signals 146 From BoxIZI PEMATUR Bit. Bith Obtain Array Match Encode Selected Match Signals) Signals 114 Array Match LSBs MSBs Logz 4 LSBs 152 Signal(s) Address Code 160

F19. Z

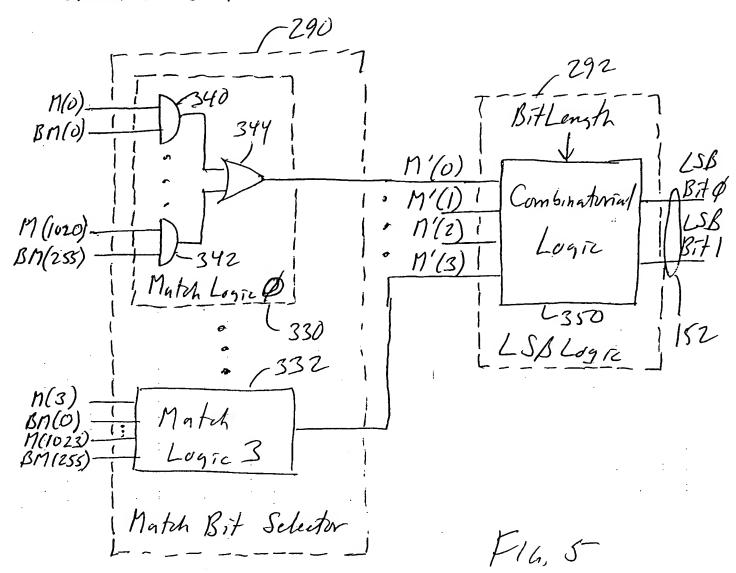


Fr 3

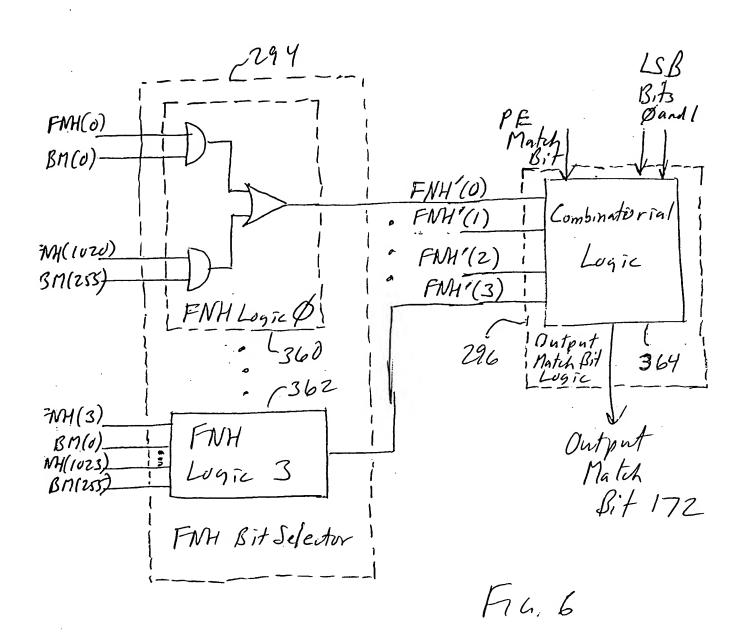
P927 Sheet 4 of 10 BitLength 310 n(i)_ M(TH) Match Bit Combiner Combiner 4:1 Match Combining Circuity

F14, 4

P927 Sheet 5 of 10



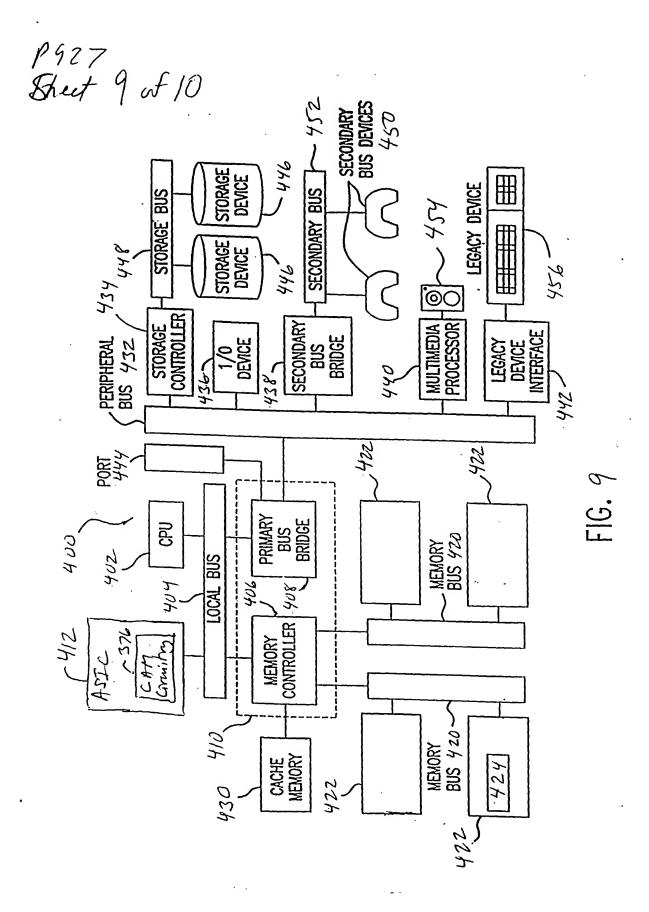
P927 Sheet 6 of 10



P927 Sheet 7 & 10

p927 Sheet & of 10 F14.8

	Upper Half of CAM >	
	Moper Match and FNH	
	13.4 Select Circuity Mppor Match Combining Circity (2:1)	
74	Born Halves Match 1 5	
5	Moper Address Encoding Circuitry	
	Encoding Circuitry 256 Entry Privity Encoder The Address	
	Encoding Circuitry	
	Lower Match and FMH	runt
	Bit Select Circuitry Lower Half of CAM	r Cr
	Array (512 Entries)	CAM



P927 Sheet 10 of 10

F14,10

